	Parameter ID HEX		Parameter Description		Min. Write Value	Max. Write Value		Request	Respon
128		SYNC Start	Start a run in SYNC mode	Func	-	-	Note: The internal, master and error counter are not changed or set to default values.	Standard	Standa
129	81	SYNC Stop	Stop a run in SYNC mode	Func	-	-	Note: The internal, master and error counter are not changed or set to default values.	Standard	Standa
			Set or read LSByte value of stop condition for				This parameter defines the number of cycles before automatic stop of a SYNC mode run. Set a zero in parameter ID 130 and ID 131 to disable		
130	82	SYNC Stop Condition	SYNC mode run	R/W	(	65536	automatic stop feature.	Standard	Standa
			Set or read MSByte value of stop condition for				This parameter defines the number of cycles before automatic stop of a SYNC mode run. Set a zero in parameter ID 130 and ID 131 to disable		
131	83	SYNC Stop Condition	SYNC mode run	R/W	(	65536	automatic stop feature.	Standard	Standa
							0=No SYNC run active		
132	84	SYNC Run Active	Read the SYNC run active information	R	-	-	1= SYNC run active	Standard	Standa
133	85	Ethernet Target IP Address 0-1	Set or read MSByte of ethernet target IP address	R/W	(	65536	Target IP address of SYNC data packages.	Standard	Standa
134		Ethernet Target IP Address 2-3	Set or read LSByte of ethernet target IP address	R/W	(		Target IP address of SYNC data packages.	Standard	Standa
135		Ethernet Target Port	Set or read ethernet target port	R/W	(	65536	Target port of SYNC data packages.	Standard	Standa
136									
137	89								
			SYNC segment quantity in each SYNC cycle. This						
			parameter is part of the SYNC data package				This parameter is sent as part of the header in UDP packages in SYNC Mode only. Reading out this parameter with standard request results in the		
138	8A	SYNC segment qty	header only.	R	-	-	reponse value 0.	Standard	Stand
			SYNC segment number in each SYNC cycle. This						
			parameter is part of the SYNC data package				This parameter is sent as part of the header in UDP packages in SYNC Mode only. Reading out this parameter with standard request results in the		
	8B	SYNC segment number	header only.	R	-	-	reponse value 0.	Standard	Stand
							Defines the Status Data to be sent. Each Bit enables or disables a seperate status information:		
140		1		1		1	Bit 0: 0 = Disable "Channel Index"; 1=Enable "Channel Index"	1	
				1		1	Bit 1: 0 = Disable Firmware Infomation; 1 = Enable Firmware Information: "Firmware Version Major", "Firmware Version Minor", "Firmware	1	
		1		1		1	Version Patch", "Firmware Version Build", "Firmware Version Variant"	1	
							Bit 2: 0 = Disable "SYNC Mode"; 1= Enable "SYNC Mode"		
							Bit 3: 0 = Disable "SYNC Cycle Time"; 1= Enable "SYNC Cycle Time"		
							Bit 4: 0 = Disable "MCU Status"; 1=Enable "MCU Status"		
			Set or read configuration of trasmitted status data				Bit 5: 0 = Disable "Board Temperature"; 1=Enable "Board Temperature"		
	8C	SYNC Status Data Setting	in each SYNC cycle	R/W	0	63	Bit 6-15: Reserved	Standard	Stand
				1 '			Defines the Runtime Data to be sent. Each Bit enables or disables a seperate runtime information:		
							Bit 0: 0 = Disable "SYNC Run Active"; 1=Enable "SYNC Run Active"		
							Bit 1: 0 = Disable "Realtime"; 1=Enable "Realtime"		
							Bit 2: 0 = Disable "Lifetime"; 1=Enable "Lifetime"		
							Bit 3: 0 = Disable "Output Counts"; 1=Enable "Output Counts"		
							Bit 4: 0 = Disable "Input Counts"; 1=Enable "Input Counts"		
							Bit 5: 0 = Disable "Output Count Rate"; 1=Enable "Output Count Rate"		
							Bit 6: 0 = Disable "Input Count Rate"; 1=Enable "Input Count Rate"		
							Bit 7: 0 = Disable "Cycle Counter Internal"; 1=Enable "Cycle Counter Internal"		
							Bit 8: 0 = Disable "Cycle Counter Master"; 1=Enable "Cycle Counter Master"		
							Bit 9: 0 = Disable "SYNC Cycle Error Counter"; 1=Enable "Cycle Error Counter"		
			Set or read configuration of trasmitted runtime				Bit 10: 0 = Disable "SYNC Com Error"; 1=Enable "SYNC Com Error"		
141	8D	SYNC Runtime Data Setting	data in each SYNC cycle	R/W	(	2047	Bit 11-15: Reserved	Standard	Stand
							Defines the spectral data to be sent:		
							0 = No spectral data		
			Set or read configuration of transmitted spectral				1 = Send MCA		
142	8E	SYNC Spectral Data Setting	data in each SYNC cycle	R/W	0	) 1	others: no spectral data	Standard	Stand
143	8F	SYNC Channel Index	Set or read the SYNC channel index	R/W	(	255	Channel index of the system for identification within a multichannel system	Standard	Stand
144							0 = No SYNC (Single channel operation)		
		1		1		1	1 = MX-Sort Master	1	
		1		1		1	2 = MX-Sort Slave	1	
				1		1	3 = MX-Sort Free-Running Slave	1	
	90	SYNC Mode	Set or read SYNC Mode	R/W	, i i i i i i i i i i i i i i i i i i i		4 = Mapping Mode	1	
1.14			Set or read LSByte of SYNC measument time for					1	
145	91	SYNC Cycle Time Low	master or mapping mode	R/W	,	65525	LSBytes of time in units of 10µs	Standard	Stand
147		ette eyele time Low	Set or read MSByte of SYNC measument time for					- consaru	Juilu
146	92	SYNC Cycle Time High	master or mapping mode	R/W			MSBytes of time in units of 10µs	Standard	Stand
140		Since eyele time tight	moster or mapping mode	19 99			photopics or dimentional of 10µ3	stanuaru	Juliu
				1		1	Error counter which is incremented if the matter cycle counter value doors't match with the interval cycle counter value of the abarral is and a	1	
147	02	SYNC Cycle Error Counter	Read SYNC Cycle Error Counter				Error counter which is incremented if the master cycle counter value doesn't match with the internal cycle counter value of the channel in case of slave operation. In Mapping Mode the counter is incremented if a trigger was received during a busy periode.	Standard	Stand
	55	Sinc Cycle Error counter	nead STINE Cycle Error Counter	ĸ	(	05535		stanuaro	stand
		1		1		1	Bitmask, each bit represents a different synchronisation error type.	1	
				1		1	Bit 0: wrong UART start or stoppbit; 0 = no error; 1 = error	1	
	L			1.		1	Bit 1: wrong UART parity bit; 0 = no error; 1 = error	I	
	94	SYNC Com Error	Read SYNC Com Error	R	-	-	Bit 2-15: reserved	Standard	Stand
148	1	1		1		1		1	
		SYNC Cycle Counter Internal	Read internal SYNC cycle counter	R	-	-	Counter of the current cycle of this channel. In slave mode, this value will be synchronised by the master, if a master is present in the system.	Standard	Stand
149			Read master SYNC cycle counter	R	-	-	This value is sent from the SYNC master to all slaves. The internal counters of the slaves will be synchronised by the master.	Standard	Stand
		SYNC Cycle Counter Master			I	1		1	
149 150	96		Initiate reset of internal, master and error counter						
149	96	SYNC Cycle Counter Master SYNC Reset		Func	-	-	Resets the internal, master and error counter to default values.	Standard	Stand
149 150	96		Initiate reset of internal, master and error counter	Func	-	-	Resets the internal, master and error counter to default values. Defines the start-up behaviour on power-up:	Standard	Stand
149 150	96		Initiate reset of internal, master and error counter	Func	-	-		Standard	Stand
149 150	96		Initiate reset of internal, master and error counter	Func R/W		-	Defines the start-up behaviour on power-up:	Standard Standard	Stand